

REMARKS

This paper is responsive to the final Office Action mailed December 27, 2006. Claims 1-9 and 11-13 are pending. Claims 1, 4, 5, 8, 9 and 12 have been amended. Support for all amended claims can be found in the specification, and no new matter has been added by these amendments. Reconsideration of the claims in view of the amendments and the following remarks is respectfully requested.

Claim Rejections Under 35 U.S.C. § 112

The Office Action rejected claim 9 under 35 U.S.C. 112, first paragraph, as failing to comply with the written description requirement. The Office Action states that claim 9 contains subject matter which was not described in the specification in such a way as to reasonably convey to one skilled in the relevant art that the inventors, at the time the application was filed, had possession of the claimed invention. Specifically, the Office Action indicates that the equation for determining C_i is not supported by the specification.

Applicants have amended the equation for determining C_i such that the summation is performed for $j=i$ to n rather than for $j=1$ as originally claimed. In view of the foregoing, withdrawal of the rejection of claim 9 under 35 U.S.C. § 112 is respectfully requested.

Claim Rejections Under 35 U.S.C. § 102

The Office Action rejected claims 1-7 under 35 U.S.C. § 102(b) as being anticipated by *Yamazaki* (US 2002/0089473). Without conceding the merits of the rejection, Applicants respectfully submit that the amended claims overcome this rejection.

Claim 1, as amended, recites in part, a "signal correction circuit comprising: a first memory to sequentially store the video data corresponding to each of the data lines in a first direction, a second memory to store first correction data which is obtained by, for each data line, multiplying each of the video data read out from the first memory by a predetermined coefficient and then sequentially accumulating the multiplied data in a second direction opposite to the first direction, and a third memory to store second correction data as the cumulative value for each of

the data lines, the second correction data being obtained by, for each data line, sequentially accumulating the first correction data read out from the second memory in the first direction."

Yamazaki discloses correcting drive signals to compensate for a voltage drop caused by wiring resistance. Specifically, *Yamazaki* teaches dividing a horizontal interval into periods, and producing correction data in each period. See for example *Yamazaki* at paragraphs [45]-[57].

Neither *Yamazaki* nor any of the other cited references, alone or in combination, teach or suggest all of the features recited in amended claim 1. Specifically, *Yamazaki* does not teach or suggest a signal correction circuit having first, second, and third memories for calculating a cumulative value and producing correction data to correct driving signals. For at least this reason, claim 1 is allowable over the cited art, as are claims 2 and 3 which derive patentability therefrom.

Independent claims 4 and 5, as amended, recite elements similar to the elements recited in amended claim 1, albeit different in other important ways. As discussed above, claim 1 is allowable over the art of record. Thus, claims 4 and 5 are also allowable over the cited art for at least the same reasons. Claims 6 and 7 depend from claim 5 and are also allowable for at least the same reasons.

In view of the foregoing, withdrawal of the rejection of claims 1-7 under 35 U.S.C. 102(b) is respectfully requested.

Claim Rejections Under 35 U.S.C. § 103

The Office Action rejected claim 8 under 35 U.S.C. § 103(a) as being unpatentable over *Yamazaki*. The Office Action rejected claim 11 under 35 U.S.C. § 103(a) as being unpatentable over *Yamazaki* in view of *Suzuki* (US 5,734,361). The Office Action rejected claims 12 and 13 under 35 U.S.C. § 103(a) as being unpatentable over *Yamazaki* in view of *Nakano* (US 2003/0025717). Without conceding the merits of the rejection, Applicants respectfully submit that the amended claims overcome this rejection.

Independent claims 8 and 12, as amended, recite elements similar to the elements recited in amended claim 1, albeit different in other important ways. As discussed above, claim 1 is allowable over the art of record. Thus, claims 8 and 12 are allowable over the cited art for at least the same reasons. Claim 11 depends from claim 8, and claim 13 depends from claim 12. Thus, claims 11 and 13 are also allowable for at least the same reasons.

In view of the foregoing, withdrawal of the rejection of claims 8 and 11-13 under 35 U.S.C. § 103(a) is respectfully requested.

CONCLUSION

In view of the foregoing, Applicants believe all claims now pending in this application are in condition for allowance and an action to that end is respectfully requested.

If the Examiner believes a telephone conference would expedite prosecution of this application, please telephone the undersigned at 206-467-9600.

Respectfully submitted,

3/27/07
Date



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